

NEW MyDac AGAINST COMPETITION / TECHNICAL FEATURES

CONFIDENTIAL / FOR INTERNAL USE ONLY -

	Cambridge DAC 100	Mus Fidelity V Dac II	Arcam R-Dac	NAD MDC-DAC1	Pro-ject DAC Box FL	Pro-ject DAC Box USB	Music Hall DAC 15.2	Peachtree DAC it	Micromega MYDAC
Price	349 €	249 €	450 €	250 €	219 €	169 €	299 €	340 €	296 €
Asynchronous	Yes	Yes	Yes	Yes	-	-	No	No	Yes
Made in	China	China	China	China	Czech Republic	Czech Republic	China	China	France
Resolution	24 bits	24 bits	24 bits	24 bits	16 bits	16 bits	24 bits	24 bits	24 bits
D/A Chip	Wolfson WM8742	Burr Brown DSD 1796	Wolfson WM8741	Burr Brown PCM1796	Burr Brown	Burr Brown TLV 320dac23	Burr Brown PCM1796	Sabre ESS ES9023	Cirrus Logic CS4351
Inputs	USB – 192kHz	USB - 96kHz	USB - 96kHz	USB - 96 kHz		USB - 44 kHz	USB - 96 kHz	USB - 96 kHz	USB - 192 kHz
	2 Coax. - 192 kHz	Coax. - 192 kHz	Coax. 192 kHz		Coax. - 44 kHz	Coax. - 44 kHz	Coax. - 192 kHz	Coax. - 192 kHz	Coax. - 192 kHz
	Opto – 192 kHz	Opto - 96 kHz	Opto. 48 kHz	Opto. - 192 kHz	Opto. - 44 kHz	Opto. - 44 kHz	Opto. - 192 kHz	Opto - 96 kHz	Opto. - 192 kHz
Power supply	External	External	External	External	External	External	External	External	Dual internal SMPS
Power consumption	5 w	N.A.	N.A.	4W	4W	4W	N.A.	2W - 1W	0,5 / 2,5 w
THD	0,0025	0,004	0,003	0,005	N.A.	0,005	0,0012	N.A.	0,002
S/N	- 113 dB	- 117 dB	< 104 dB	>-100dB	N.A.	N.A.	110 dB	120 dB	> 110 dB
Jitter in SPDIF	< 130 pS	< 170 pS	N.A.	N.A.	N.A.	N.A.	N.A.	N.A.	< 50 pS ***
USB audio class	2	1	1 & 2	1	N.A.	1	1	1	1 & 2

* MyDac is the ONLY product in this range offering a designed for audio, low-noise power supply. There are 2 dedicated power supplies, one for the digital section and one, with a totally unique design, for the audio section. No low-end shelf type external power transformer here. This also allows for very low power consumption in STBY mode (<0,5W).

** The sealed ABS enclosure have been designed on purpose to control the electromagnetic field on the board level without any interference from a electrically conductive casing.

*** The SPDIF extremely low jitter performance is achieved thanks to careful layout and not using any PLL in the data recovery signal path.